

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S41	24	((d or delay) near3 (flip or flip \$1flop\$1)) fpga fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/08 16:13
S40	20	(d near3 (flip or flip \$1flop\$1)) fpga fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/08 16:13
S39	3	"717"/\$.ccls. (fpga same (co \$1process\$4 or co\$1simulat\$4)) path filter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:21
S38	31	"717"/\$.ccls. fpga path filter (co\$1process\$4 or co\$1simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:20
S37	83	"717"/\$.ccls. fpga path (co \$1process\$4 or co\$1simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:20
S36	0	717/?\$.ccls. fpga path (co \$1process\$4 or co\$1simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:20
S35	0	717/?\$.ccls. fpga path filter (co \$1process\$4 or co\$1simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:20
S34	4	712/35.ccls. fpga path filter (co\$1process\$4 or co\$1simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:13

S33	8	710/305.ccls. fpga path filter (co\$1process\$4 or co\$1simulat \$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:07
S32	24	710/305.ccls. fpga path filter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:07
S30	28	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4)) (radar\$4 or numeri\$4 or sine or wave) (port or filter) path (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:02
S29	43	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4 or partition\$4)) (radar\$4 or numeri\$4 or sine or wave) (port or filter) path (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:02
S28	43	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4 or partition\$4)) (radar\$4 or numeri\$4 or sine or wave) path (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:01

S27	2	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4 or partition\$4)) (radar\$4 or numeri\$4 or sine or wave) patch (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:01
S26	57	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4 or partition\$4)) (radar\$4 or numeri\$4 or sine or wave) (fft \$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:01
S25	39	((fpga same (cpu or processor or dsp) same (co \$1process\$4 or co\$1simulat\$4)) (radar\$4 or numeri\$4 or sine or wave) (fft \$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/07 14:00
S24	61	(radar\$4 or numeri\$4 or sine or wave) ((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) (fft \$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:33
S23	33	(radar\$4 or numeri\$4 or sine or wave) ((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) (fft \$2 or fourier) path	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:31

S21	59	((radar\$4 or numeri\$4 or sine or wave) ((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) (fft \$2 or fourier) (port or filter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:25
S19	24	((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) ((radar\$4 or numeri\$4 or sine or wave) same (process \$4 or simulat \$4)) (fft\$2 or fourier) port filt \$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:17
S18	28	((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) ((radar\$4 or numeri\$4 or sine or wave) same (process \$4 or simulat \$4)) (fft\$2 or fourier) port	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:16

S17	40	((cpu or processor or dsp) same (co or co\$1process \$4 or co \$1simulat\$4) same fpga) ((radar\$4 or numeri\$4 or sine or wave) same (process \$4 or simulat \$4)) (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 10:16
S6	109	((cpu or processor or dsp) near9 fpga) ((radar\$4 or numeri\$4 or sine or wave) same simulat \$4) (fft\$2 or fourier)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:36
S5	47	((cpu or processor or dsp) near9 fpga) ((radar\$4 or numeri\$4 or sine or wave) same simulat \$4) fft\$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:36
S4	401	((cpu or processor or dsp) near9 fpga) ((radar\$4 or numeri\$4 or sine or wave) same simulat \$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:35
S3	1135	((cpu or processor or dsp) near9 fpga) ((radar\$4 or number\$4 or sine or wave) same simulat \$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:35

S2	1869	((cpu or processor or dsp) near9 fpga) ((radar\$4 or number\$4 or signal) same simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:34
S1	16691	((cpu or processor or dsp) near9 fpga)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/04 09:33

**7/8/2009 5:30:07 PM****C:\Documents and Settings\tdao1\My Documents\EAST\Workspaces\0720 10 720 614 simulation speedup.wsp**